Towards a causal and compositional operational semantics of programming languages

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Consider this program mp:

$$data = flag = 0$$

$$data := 17; || r \leftarrow flag;$$

$$flag := 1 || if(r == 1) \{v \leftarrow data\}$$

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Possible execution traces on my computer:

 $\blacktriangleright W_{\texttt{data}:=17} \cdot W_{\texttt{flag}:=1} \cdot R_{\texttt{flag}=1} \cdot R_{\texttt{data}=17}$

Consider this program mp:

$$\begin{array}{c} \texttt{data} = \texttt{flag} = \texttt{0} \\ \texttt{data} := \texttt{17}; & \mid \textbf{r} \leftarrow \texttt{flag}; \\ \texttt{flag} := \texttt{1} & \mid \texttt{if}(\textbf{r} ==\texttt{1})\{\textbf{v} \leftarrow \texttt{data}\} \end{array}$$

Possible execution traces on my computer:

- $\blacktriangleright W_{data:=17} \cdot W_{flag:=1} \cdot R_{flag=1} \cdot R_{data=17}$
- ► W_{data:=17} · R_{flag=0} · W_{flag:=1}
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$$data = flag = 0$$

$$data := 17; \parallel r \leftarrow flag;$$

$$flag := 1 \parallel if(r == 1) \{ v \leftarrow data \}$$

$$\blacktriangleright$$
 W_{data:=17} · W_{flag:=1} · R_{flag=1} · R_{data=17}

$$data = flag = 0$$

$$data := 17; \parallel r \leftarrow flag;$$

$$flag := 1 \parallel if(r == 1) \{ v \leftarrow data \}$$

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 W_{data:=17} · R_{flag=0} · W_{flag:=1}

$$data = flag = 0$$

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 W_{data:=17} · W_{flag:=1} · R_{flag=1} · R_{data=17}

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 W_{data:=17} · R_{flag=0} · W_{flag:=1}

$$\blacktriangleright$$
 W_{flag:=1} · R_{flag=1} · R_{data=0}

data = flag = 0
data := 17;
$$\parallel r \leftarrow$$
flag;
flag := 1 $\parallel if(r == 1) \{ v \leftarrow$ data $\}$

$$\blacktriangleright$$
 W_{data:=17} · W_{flag:=1} · R_{flag=1} · R_{data=17}

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Possible execution traces on my phone:

A different architecture, much harder to reason about...

$$\begin{cases} \mathtt{W}_{\texttt{flag}:=1} \cdot \mathtt{W}_{\texttt{data}:=17} \cdot \mathtt{R}_{\texttt{flag}=1} \cdot \mathtt{R}_{\texttt{data}=17} \\ \mathtt{W}_{\texttt{flag}:=1} \cdot \mathtt{R}_{\texttt{flag}=1} \cdot \mathtt{W}_{\texttt{data}:=17} \cdot \mathtt{R}_{\texttt{data}=17} \\ \mathtt{W}_{\texttt{data}:=17} \cdot \mathtt{W}_{\texttt{flag}:=1} \cdot \mathtt{R}_{\texttt{flag}=1} \cdot \mathtt{R}_{\texttt{data}=17} \end{cases}$$

$$\left\{ \begin{array}{c} \mathtt{W}_{\texttt{flag}:=1} \cdot \mathtt{R}_{\texttt{flag}=1} \cdot \mathtt{R}_{\texttt{data}=0} \cdot \mathtt{W}_{\texttt{data}:=17} \end{array} \right.$$

$$\begin{cases} R_{\texttt{flag}=0} \cdot \texttt{W}_{\texttt{data}:=17} \cdot \texttt{W}_{\texttt{flag}:=1} \\ \texttt{W}_{\texttt{data}:=17} \cdot R_{\texttt{flag}=0} \cdot \texttt{W}_{\texttt{flag}:=1} \\ R_{\texttt{flag}=0} \cdot \texttt{W}_{\texttt{flag}:=1} \cdot \texttt{W}_{\texttt{data}:=17} \end{cases}$$

$$\begin{cases} \mathbb{W}_{\texttt{flag}:=1} \cdot \mathbb{W}_{\texttt{data}:=17} \cdot \mathbb{R}_{\texttt{flag}=1} \cdot \mathbb{R}_{\texttt{data}=17} & \mathbb{W}_{\texttt{flag}:=1} & \mathbb{W}_{\texttt{data}:=17} \\ \mathbb{W}_{\texttt{flag}:=1} \cdot \mathbb{R}_{\texttt{flag}=1} \cdot \mathbb{W}_{\texttt{data}:=17} \cdot \mathbb{R}_{\texttt{data}=17} & \bigtriangledown & \bigvee \\ \mathbb{W}_{\texttt{data}:=17} \cdot \mathbb{W}_{\texttt{flag}:=1} \cdot \mathbb{R}_{\texttt{flag}=1} \cdot \mathbb{R}_{\texttt{data}=17} & \mathbb{R}_{\texttt{flag}=1} \to \mathbb{R}_{\texttt{data}=17} \end{cases}$$

$$\left\{ \begin{array}{l} \mathtt{W}_{\texttt{flag}:=1} \cdot \mathtt{R}_{\texttt{flag}=1} \cdot \mathtt{R}_{\texttt{data}=0} \cdot \mathtt{W}_{\texttt{data}:=17} \end{array} \right.$$

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$$\left\{ \begin{array}{l} \mathtt{W}_{\texttt{flag}:=1} \cdot \mathtt{R}_{\texttt{flag}=1} \cdot \mathtt{R}_{\texttt{data}=0} \cdot \mathtt{W}_{\texttt{data}:=17} \end{array} \right.$$

$$\begin{array}{c|c} \mathbb{W}_{\texttt{flag}:=1} & \mathbb{R}_{\texttt{data}=0} \\ & & & & \\ & & & & \\ \mathbb{K}_{\texttt{flag}=1} & \mathbb{W}_{\texttt{data}:=17} \end{array}$$

$$\begin{cases} R_{\texttt{flag}=0} \cdot \mathbb{W}_{\texttt{data}:=17} \cdot \mathbb{W}_{\texttt{flag}:=1} \\ \mathbb{W}_{\texttt{data}:=17} \cdot R_{\texttt{flag}=0} \cdot \mathbb{W}_{\texttt{flag}:=1} \\ R_{\texttt{flag}=0} \cdot \mathbb{W}_{\texttt{flag}:=1} \cdot \mathbb{W}_{\texttt{data}:=17} \end{cases}$$

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$$\left\{ \begin{array}{ccc} \mathbb{W}_{\texttt{flag}:=1} \cdot \mathbb{R}_{\texttt{flag}=1} \cdot \mathbb{R}_{\texttt{data}=0} \cdot \mathbb{W}_{\texttt{data}:=17} & & & \mathbb{W}_{\texttt{flag}:=1} & \mathbb{R}_{\texttt{data}=0} \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & & & \\ & & & & \\ & & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & & \\ & & & & \\ & & & & & \\ & & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & &$$

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Sets of partial orders and event structures

The set of partial orders describes the semantics of mp:

$$\begin{array}{c} & \mathbb{W}_{\texttt{flag}:=1} \\ & \forall \\ \mathbb{W}_{\texttt{flag}:=1} & \mathbb{W}_{\texttt{data}:=17} & \mathbb{R}_{\texttt{flag}=1} & \mathbb{R}_{\texttt{flag}=0} & \mathbb{W}_{\texttt{data}:=17} \\ & \forall & \forall & , & \forall \\ \mathbb{K}_{\texttt{flag}=1} \rightarrow \mathbb{R}_{\texttt{data}=17} & \mathbb{R}_{\texttt{data}=0} & \mathbb{W}_{\texttt{data}:=1} \\ & & \forall \\ & \mathbb{W}_{\texttt{data}:=17} \end{array}$$

Sets of partial orders and event structures

The set of partial orders describes the semantics of mp:

$$\begin{pmatrix} & & & & & \\ & & & & \\ & &$$

This set of partial orders can be summed by an event structure:

$$\begin{array}{cccc} \mathbb{W}_{\texttt{data}:=17} & \mathbb{W}_{\texttt{flag}:=1} & \sim \mathbb{R}_{\texttt{flag}=0} \\ \downarrow & \downarrow & \downarrow \\ \mathbb{R}_{\texttt{data}=17} & \leftarrow \mathbb{R}_{\texttt{flag}=1} & \mathbb{W}_{\texttt{flag}:=1} \\ & & \searrow \\ \mathbb{R}_{\texttt{data}=0} & & \\ & & \downarrow \\ \mathbb{W}_{\texttt{data}:=17} \end{array}$$

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This talk

1. Modelling a first-order programming language. With relaxed shared memory.

2. When actions become contextual. An introduction to game semantics for higher-order languages.

3. Interpretating a higher-order language, adequately. With concurrency & non-determinism.

I. MODELLING A FIRST-ORDER PROGRAMMING LANGUAGE

An ARM-like semantics for a toy language

An assembly language with relaxed semantics

Syntax. Idents split in thread-local registers and global variables.

$$e ::= r | e + e | \dots$$

 $t ::= fence; t | x := e; t | r \leftarrow x; t$
 $p ::= t || \dots || t$

Actions. We observe the following actions from the programs:

$$\Sigma ::= \mathbb{W}_{\mathbf{x}:=k} \mid \mathbb{R}_{\mathbf{x}=k} \mid \texttt{fence}.$$

Semantics. Described by a labeled transition system on states p, μ :

$$\langle p @ \mu \rangle \xrightarrow{\ell \in \Sigma} \langle p' @ \mu' \rangle.$$
 $(\mu, \mu' : \operatorname{Var} \to \mathbb{N})$

It is **relaxed**: operations on independent variables can be reordered.

Thread rules: $\langle t @ \mu \rangle \xrightarrow{\ell} \langle t' @ \mu' \rangle$:

$$\langle \mathtt{x} := k ; t @\mu \rangle \xrightarrow{\mathtt{W}_{\mathtt{x} := k}} \langle t @\mu [\mathtt{x} := k] \rangle$$

Thread rules: $\langle t @ \mu \rangle \xrightarrow{\ell} \langle t' @ \mu' \rangle$:

$$\langle \mathtt{x} := k; t @\mu
angle \xrightarrow{\mathtt{W}_{\mathtt{x}:=k}} \langle t @\mu[\mathtt{x} := k]
angle \qquad \langle \texttt{fence}; t @\mu
angle \xrightarrow{\mathtt{fence}} \langle t @\mu
angle$$

Thread rules: $\langle t @ \mu \rangle \xrightarrow{\ell} \langle t' @ \mu' \rangle$:

$$\begin{array}{l} \langle \mathbf{x} := k; t @\mu \rangle \xrightarrow{ \mathbb{W}_{\mathbf{x} := k} } \langle t @\mu[\mathbf{x} := k] \rangle & \overline{\langle \texttt{fence}; t @\mu \rangle \xrightarrow{ \texttt{fence} } \langle t @\mu \rangle} \\ \\ \\ \hline \frac{\langle t @\mu \rangle \xrightarrow{\ell} \langle t' @\mu' \rangle \quad \ell \neq \texttt{fence} \quad \texttt{var}(\ell) \neq \mathbf{x}}{\langle \mathbf{x} := k; t @\mu \rangle \xrightarrow{\ell} \langle \mathbf{x} := k; t' @\mu' \rangle} \end{array}$$

Thread rules: $\langle t @ \mu \rangle \xrightarrow{\ell} \langle t' @ \mu' \rangle$:

$$\langle \mathbf{x} := k; t \mathbf{0} \mu \rangle \xrightarrow{\mathbf{W}_{\mathbf{x}:=k}} \langle t \mathbf{0} \mu [\mathbf{x} := k] \rangle \qquad \langle \texttt{fence}; t \mathbf{0} \mu \rangle \xrightarrow{\texttt{fence}} \langle t \mathbf{0} \mu \rangle$$

$$\frac{\langle t @\mu \rangle \xrightarrow{\ell} \langle t' @\mu' \rangle \quad \ell \neq \texttt{fence} \quad \texttt{var}(\ell) \neq \texttt{x}}{\langle \texttt{x} := k; t @\mu \rangle \xrightarrow{\ell} \langle \texttt{x} := k; t' @\mu' \rangle}$$

And then:

$$\frac{\langle t_i @ \mu \rangle \stackrel{\ell}{\to} \langle t'_i @ \mu' \rangle}{\langle t_1 \parallel \dots \parallel t_i \parallel \dots \parallel t_n @ \mu \rangle \stackrel{\ell}{\to} \langle t_1 \parallel \dots \parallel t'_i \parallel \dots \parallel t_n @ \mu' \rangle}$$

Thread rules: $\langle t @ \mu \rangle \xrightarrow{\ell} \langle t' @ \mu' \rangle$:

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$$\frac{\langle t @\mu \rangle \xrightarrow{\ell} \langle t' @\mu' \rangle \quad \ell \neq \texttt{fence} \quad \texttt{var}(\ell) \neq \texttt{x}}{\langle \texttt{x} := k; t @\mu \rangle \xrightarrow{\ell} \langle \texttt{x} := k; t' @\mu' \rangle}$$

And then:

$$\frac{\langle t_i @ \mu \rangle \stackrel{\ell}{\to} \langle t'_i @ \mu' \rangle}{\langle t_1 \parallel \dots \parallel t_i \parallel \dots \parallel t_n @ \mu \rangle \stackrel{\ell}{\to} \langle t_1 \parallel \dots \parallel t'_i \parallel \dots \parallel t_n @ \mu' \rangle}$$

This generates the operational (partial) traces:

$$\operatorname{Tr}(\boldsymbol{p},\boldsymbol{\mu}) = \{\ell_1 \dots \ell_n \mid \langle \boldsymbol{p} \, \boldsymbol{\mathbb{O}} \, \boldsymbol{\mu} \rangle \xrightarrow{\ell_1} \dots \xrightarrow{\ell_n} \langle \boldsymbol{p}' \, \boldsymbol{\mathbb{O}} \, \boldsymbol{\mu}' \rangle \}.$$

Labeled event structures

Definition A (Σ -labeled) event structure is a tuple $(E, \leq_E, \sharp_E, \ell : E \to \Sigma)$ where (E, \leq_E) is a partial order and \sharp_E is a symmetric relation on E, satisfying finite causes and conflict inheritance.

$$\begin{array}{ccc} a & b \\ \downarrow & \searrow & \forall \\ c & d \\ \psi \\ e \end{array}$$

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► Configurations are downclosed, conflict-free subsets of E.
𝒞(E) is the set of configurations of E.

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 $\begin{array}{c} \downarrow \swarrow \\ \bullet \\ c \\ \downarrow \end{array} \\ d \\ \end{array}$

е

A trace of E is a linearisation of a configuration of E.
 Tr(E) is the set of traces of E (can be seen as a subset of Σ*).

Our goal: a mapping $\llbracket \cdot \rrbracket$ from states to event structures s.t.:

$$\operatorname{Tr}(\boldsymbol{\rho}, \mu) = \operatorname{Tr}[\![\boldsymbol{\rho}, \mu]\!].$$

An overview of the semantics

1. Thread semantics: context is left open (and unknown)

2. Final semantics: context is assumed empty Compute interactions with memory:

$$\begin{array}{ccccc} \mathbb{W}_{\texttt{data}:=17} & \mathbb{W}_{\texttt{flag}:=1} & \sim & \mathbb{R}_{\texttt{flag}=0} \\ & & & & & & \\ & & & & & & \\ \mathbb{R}_{\texttt{data}=17} & & \mathbb{R}_{\texttt{flag}=1} & & \mathbb{W}_{\texttt{flag}:=1} \\ & & & & & \\ & & & & \\ & & & & \\ & & & & & \\ & & & & \\ & & & & \\ & & & & & \\ & & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ &$$

Thread semantics

Fences. $\llbracket \text{fence}; t \rrbracket = \text{fence} \cdot \llbracket t \rrbracket$ $(\leq_{\ell \cdot E} = \leq_E \cup \{(\ell, \ell')\})$ fence $\[t \] \]$

Thread semantics

Fences. $\llbracket fence; t \rrbracket = fence \cdot \llbracket t \rrbracket$ fence $\langle \leq_{\ell \cdot E} = \leq_E \cup \{(\ell, \ell')\})$ $\llbracket t \rrbracket$

Writes. $\llbracket x := k; t \rrbracket = \mathbb{W}_{x:=k}; \llbracket t \rrbracket$ $(\leq_{\ell;E} = \leq_E \cup \{(\ell, \texttt{fence}), (\ell, \ell') \mid \texttt{var}(\ell) = \texttt{var}(\ell')\}).$
Thread semantics

Fences.
$$\llbracket fence; t \rrbracket = fence \cdot \llbracket t \rrbracket$$
 \forall $(\leq_{\ell \cdot E} = \leq_E \cup \{(\ell, \ell')\})$ $\llbracket t \rrbracket$

fonco

Writes.
$$\llbracket x := k; t \rrbracket = W_{x:=k}; \llbracket t \rrbracket$$

 $(\leq_{\ell;E} = \leq_E \cup \{(\ell, \texttt{fence}), (\ell, \ell') \mid \texttt{var}(\ell) = \texttt{var}(\ell')\}).$

Reads. $[\![r \leftarrow x; t]\!] = \sum_{n \in \mathbb{N}} \mathbb{R}_{x=n}; [\![t[n/r]]\!]$

$$\begin{array}{cccc} \mathbf{R}_{\mathbf{x}=\mathbf{0}} & \mathbf{R}_{\mathbf{x}=\mathbf{1}} & \dots \\ & \swarrow \begin{bmatrix} t [\mathbf{0}/\mathbf{r}] \end{bmatrix}^{\mathbf{b}} & \checkmark \begin{bmatrix} t [\mathbf{1}/\mathbf{r}] \end{bmatrix}^{\mathbf{b}} & \dots \end{array}$$

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Thread semantics

Fences.
$$\llbracket fence; t \rrbracket = fence \cdot \llbracket t \rrbracket$$
 \forall
 $(\leq_{\ell \cdot E} = \leq_E \cup \{(\ell, \ell')\})$
 $\llbracket t \rrbracket$

fanaa

Writes.
$$\llbracket x := k; t \rrbracket = \mathbb{W}_{x:=k}; \llbracket t \rrbracket$$

 $(\leq_{\ell;E} = \leq_E \cup \{(\ell, \texttt{fence}), (\ell, \ell') \mid \texttt{var}(\ell) = \texttt{var}(\ell')\}).$

Reads. $[\![r \leftarrow x; t]\!] = \sum_{n \in \mathbb{N}} R_{x=n}; [\![t[n/r]]\!]$

$$\begin{array}{cccc} \mathbf{R}_{\mathbf{x}=\mathbf{0}} & \mathbf{R}_{\mathbf{x}=\mathbf{1}} & \dots \\ & \swarrow \begin{bmatrix} t [\mathbf{0}/\mathbf{r}] \end{bmatrix}^{\mathbf{b}} & \checkmark \begin{bmatrix} t [\mathbf{1}/\mathbf{r}] \end{bmatrix}^{\mathbf{b}} & \dots \end{array}$$

Program. No interaction: $\llbracket t_1 \parallel \ldots \parallel t_n \rrbracket = \llbracket t_1 \rrbracket \parallel \ldots \llbracket t_n \rrbracket$.

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Wiring memory behaviour

The memory behaviour is specified through consistent traces:

$$C_{\mu} ::= \mathtt{W}_{\mathtt{x}:=k} \cdot C_{\mu[\mathtt{x}:=k]} \mid \mathtt{fence} \cdot C_{\mu} \mid \mathtt{R}_{\mathtt{x}=\mu(\mathtt{x})} \cdot C_{\mu}$$

Theorem

For a machine state (p, μ) , $Tr(p, \mu) = Tr[[p]] \cap C_{\mu}$.

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But I promised an e.s. $[\![p, \mu]\!]!$ (causally account for memory)

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Theorem

For a machine state (p, μ) , $Tr(p, \mu) = Tr[\![p]\!] \cap C_{\mu}$.

But I promised an e.s. $[\![p, \mu]\!]!$ (causally account for memory)

The causal account of memory can be defined as:

$$\mathscr{C}_{\mu} = \{ \mathbf{q} \mid \operatorname{Tr}(\mathbf{q}) \in C_{\mu} \} \in \operatorname{Set}(\mathsf{PO}).$$

How to combine $\llbracket p \rrbracket$ and \mathscr{C}_{μ} ?

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ES

 $\llbracket p \rrbracket$

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ES Set(PO) $\llbracket p \rrbracket \qquad \mathscr{C}_{\mu}$







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A partial product on partial orders

Given two partial orders $\leq_{\mathbf{q}}, \leq_{\mathbf{q}'}$ on the same carrier set, write:

$$\mathbf{q} \wedge \mathbf{q}' = egin{cases} (\mathbf{q} \cup \mathbf{q}')^* & ext{if a partial order} \ ext{undefined} & ext{otherwise} \end{cases}$$

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... generating a product on event structures

For $P, Q \in \text{Sets}(PO)$, let:

$$P \star Q = \{p \land q \mid p \in P, q \in Q\}.$$

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Theorem

Both operations are categorical products.

Note:

$$\operatorname{Tr}(E \star E') = \operatorname{Tr}(E) \cap \operatorname{Tr}(E')$$

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A final model

Define
$$\llbracket p, \mu \rrbracket = \Pr(\mathscr{C}(\llbracket p \rrbracket) \star \mathscr{C}_{\mu})$$
. We have:
 $\operatorname{Tr}\llbracket p, \mu \rrbracket = \operatorname{Tr}\llbracket p \rrbracket \cap \operatorname{Tr}\llbracket \mathscr{C}_{\mu} \rrbracket = \operatorname{Tr}\llbracket p \rrbracket \cap \mathcal{C}_{\mu} = \operatorname{Tr}(p, \mu).$

Yields the desired result:

$$\llbracket \texttt{mp}, (\texttt{x} \mapsto \texttt{0}) \rrbracket = \begin{array}{cccc} & \mathbb{W}_{\texttt{data}:=17} & \mathbb{W}_{\texttt{flag}:=1} & \sim \mathbb{R}_{\texttt{flag}=0} \\ & \checkmark & \checkmark & \checkmark \\ & \mathbb{R}_{\texttt{data}=17} & \sim \mathbb{R}_{\texttt{flag}=1} & \mathbb{W}_{\texttt{flag}:=1} \\ & & \ddots & \checkmark \\ & \mathbb{R}_{\texttt{data}=0} \\ & & \checkmark \\ & \mathbb{W}_{\texttt{data}:=17} \end{array}$$

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Wrapping up

This architecture is a (huge) simplification of ARM v8.0.
 We can also model SC, TSO, ...

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Wrapping up

- This architecture is a (huge) simplification of ARM v8.0.
 We can also model SC, TSO, ...
- By changing C_μ we get more or less compact event structures that can be useful for verification. (Implementation in Herd in progress)
- The treatment of reorderings should make the model useful to prove properties of architectures (eg. Data-Race-Freedom theorems.)

II. WHAT ABOUT NON-FIRST ORDER LANGUAGES?

5mins of game semantics a day keeps the syntax away

Imagine now our threads look like:

```
alloc(x);
alloc(y);
r \leftarrow x;
if(r = 1){y := 1}
dealloc(x); dealloc(y)
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Implicit allocation rules give Σ some structure:



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Interaction thread/memory is an interaction client/server:



memory















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The protocol is described by the following partial order:



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Protocols as types

Interaction thread/memory is an interaction client/server:



The protocol is described by the following partial order:



Such a partial order with polarity annotations is a **game**. What is an event structure labelled by a game?

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Agent (or pre-strategy)

A **agent** on a A is an e.s. S and a labelling $\sigma: S \rightarrow A$ s.t.:

- 1. (Respects the rules) σ maps configurations of S to down-closed subsets of A
- 2. (Linearity) σ is injective on configurations.



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No explicit names: each event is below a unique a, in the game Towards a causal and compositional operational semantics of programming languages. Simon Castellan





Agents can be described by terms of the pi-calculus:

```
a: A \vdash a(x, rtt, rff).
```

q



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 $a: A \vdash a(x, rtt, rff). \overline{x}(tt, ff). (tt(). \overline{rff} \parallel ff(). \overline{rtt}).$



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Copycat, or the asynchronous forwarder

Given a game A, we write A^{\perp} for its **dual**. (polarity reversed)

For $B = \bigcup_{\substack{b' \\ \text{tt}}} q$, the **copycat** on B is the agent c_B :



(corresponding to the term:

 $a: B^{\perp}, b: B \vdash b(rtt, rff). \overline{a}(tt, ff). (tt(). \overline{rtt} \parallel tt(). \overline{rtt}).$

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An agent on $A^{\perp} \parallel B$ can be viewed as an agent from A to B:

 $\sigma: S \to A^{\perp} \parallel B \qquad \Leftrightarrow \qquad \iota: A^{\perp}, o: B \vdash P.$

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Such agents can be **composed**:

 $\sigma: S \to A^{\perp} \parallel B \quad \tau: T \to B^{\perp} \parallel C \Longrightarrow \tau \odot \sigma: T \odot S \to A^{\perp} \parallel C$

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In two steps:

- 1. Interaction of the common parts of σ and τ
- 2. Hiding of the events on *B*, invisible after composition.

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Composition: a bird's eye view

1. Interaction relies on the product of agents, generalizing the product of labelled e.s.

 \rightarrow Interaction σ and τ gives

$$\tau \circledast \sigma : T \circledast S \to A \parallel B \parallel C.$$

2. Hiding relies on projejection of event structures: events in *B* become invisible.

$$\tau \odot \sigma : T \circledast S \downarrow V \to A \parallel B \parallel C \qquad V = \tau \circledast \sigma^{-1}(A \parallel C).$$

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$$\sigma = \operatorname{\mathfrak{C}}_{\mathsf{C}}^{\mathsf{q}} : \emptyset^{\perp} \parallel B \qquad \tau = \mathfrak{c}_{\mathsf{B}} : B^{\perp} \parallel B.$$

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The interaction gives:



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$$\sigma = \operatorname{\mathfrak{C}}_{B} : \emptyset^{\perp} \parallel B \qquad \tau = \operatorname{\mathfrak{C}}_{B} : B^{\perp} \parallel B.$$

The interaction gives:



$$\sigma = \operatorname{\mathfrak{C}}_{B}^{q} : \emptyset^{\perp} \parallel B \qquad \tau = \operatorname{\mathfrak{C}}_{B} : B^{\perp} \parallel B.$$

The interaction gives:



σ not invariant under the asynchronous forwarder.

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Asynchrous agents, or *strategies*

```
Which agents \sigma satsify c_A \odot \sigma \cong \sigma?

Definition

A strategy is an agent \sigma : S \to A such that

1. S only adds immediate causal links \odot \twoheadrightarrow \oplus

2. S is cannot ignore (or duplicate) negative events.
```

```
Theorem (Rideau, Winskel)
```

An agent σ is a strategy if and only if $\mathbf{c}_A \odot \sigma \cong \sigma$.

 \rightarrow Games and strategies model linear languages (compact-closed category).

III. INTERPRETING FUNCTIONAL PROGRAMMING LANGUAGES

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Local injectivity and copy indices

To represent nonlinear agents:



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The labelling to $B \Rightarrow B$ fails local injectivity.

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Local injectivity and copy indices

To represent nonlinear agents:



The labelling to $B \Rightarrow B$ fails local injectivity.

 \rightarrow We make the game bigger.

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To compose, Opponent must be allowed to be nonlinear as well:



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But strategies should be uniform.

(Uniformity is defined by using event structures with symmetry.)

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But strategies should be uniform.

(Uniformity is defined by using event structures with symmetry.)

Theorem (C., Clairambault, Winskel)

The following structure CHO is a model of higher-order computation:

- Types are interpreted by games,
- Terms $\Gamma \vdash M$: A are interpreted by uniform strategies !($\llbracket \Gamma \rrbracket^{\perp} \parallel \llbracket A \rrbracket$),
- Composition is: interaction + hiding.

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An example of higher-order

Consider the *call-by-name* program

$$\texttt{strict} = \lambda f. \text{ new } r \text{ in } f(r := 1; 1); !r = 1 : (\mathbb{N} \Rightarrow \mathbb{N}) \Rightarrow \mathbb{B}.$$



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An example of higher-order

Consider the call-by-name program

$$\texttt{strict} = \lambda f. \text{ new } r \text{ in } f(r := 1; 1); !r = 1 : (\mathbb{N} \Rightarrow \mathbb{N}) \Rightarrow \mathbb{B}.$$



If f is concurrent with control operators, strict exhibits a race. Theorem

CHO can interpret concurrent languages, adequately for may:

 $M \Downarrow \Leftrightarrow \llbracket M \rrbracket$ contains a positive move.

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However, in nondetermistic languages convergence is more subtle:

 $M = \lambda b$. (if b then loop else tt).

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 $\llbracket M \rrbracket \circledast$ choice:



B

However, in nondetermistic languages convergence is more subtle:

 $M = \lambda b$. (if b then loop else tt).

Does *M* choice converge?

 $\llbracket M
rbracket \odot$ choice :



TR

As a result: $[M \ choice] = [tt].$ Model inadequate for *non-angelic* convergences!

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Essential events

Idea: never hide essential events appearing in a conflict:

 $\llbracket M \rrbracket \otimes$ choice: $\Bbb B$



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Idea: never hide essential events appearing in a conflict:

 $\llbracket M \rrbracket \odot$ choice: $\Bbb B$



Strategies become partial maps $S \rightarrow A$ (with internal events). Theorem (C., Clairambault, Hayman, Winskel) The partial strategies $\tau \circledast \sigma$ and $\tau \odot \sigma$ are weakly bisimilar. \rightarrow Partial hiding does not lose behaviour up to weak bisimilarity.

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The category $\rm CHO_{\odot}$

Despite not hiding *everything*, we still get a category:

Theorem (C.)

The following model CHO_{\odot} is a model of higher-order computation:

- Types are interpreted as in CHO,
- Terms are interpreted by strategies with internal events,
- Composition is: interaction + partial hiding.

Moreover CHO_{\odot} interprets nondeterministic languages, adequately for non-angelic convergences (must, fair), ...

 $\ln CHO_{\odot}$, one can define *intensional*, *causal*, *compositional* semantics for a wide variety of languages.

Related work

Earlier work / inspirations:

- Melliès's asynchronous games. Traces augmented with 2-dimensional tiles representing independence.
- Curien, Faggian, Piccolo, I-nets.
 Partial order representation for ludics.

Parallel works:

- Hirschowitz et. al.: preseheaves over graphs. (no hiding) Gives intensional models of π-calculus fully abstract for fair convergence.
- Ong, Tsukada: presheaves over plays.
 Models of nondeterministic, concurrent languages.

On causal models for weak memory models:

► Jeffrey & Riley, Brookes *et. al.*, Pichon *et. al.*

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Extensions / Other work

Axis of development:

1. Understanding the structure of strategies. Which strategies are expressible using which effects? \rightarrow Fully abstract models of extensions of PCF. (With Clairambault, and Winskel)

2. Adding quantitive information.

- probabilities (full abstraction for probabilistic PCF) (With Clairambault, Paquet and Winskel)
- quantum (WIP by Clairambault, De Visme, Winskel)

3. Modelling complex languages. Work in progress:

- Complex memory models (with Alglave and Madiot),
- Session π -calculus (with Clairambault and Yoshida).

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